

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number	09/823,148
Filing Date	March 29, 2001
First Named Inventor:	Lynn R. Youngs
Art Unit	2133
Examiner Name	John P. Trimmings
Attorney Docket Number	04860.P2577

Sheet 1 MAR 25 2004 1

NON PATENT LITERATURE DOCUMENTS

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Initials*

Cite
No¹

Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published

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S. YURASH et al., "Automatic Test Pattern Generation Comes of Age," Electronic Engineering, Vol. 63, No. 777, Sept. 1991, London GB, pp. 35-36 & 38

E.B. Eichelberger et al., "A Logic Design Structure for LSI Testability," Reprinted from The Proceedings of the 14th Design Automation Conference by the Institute of Electrical and Electronics Engineers, Inc., pp. 206-212 (1997)

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John P. Trimmings

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Substitute for Form 1449/PTO

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1 of 2

U.S. PATENT DOCUMENTS

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
		US-	5,689,517	11-18-1997	Ruparel	
		US-	4,495,628	01-22-1985	Zasio	
		US-	4,495,629	01-22-1985	Zasio et al.	
		US-	4,912,709	03-27-1990	Teske et al.	
		US-	5,003,204	03-26-1991	Cushing et al.	
		US-	5,130,568	07-14-1992	Miller et al.	
		US-	5,257,223	10-26-1993	Dervisoglu	
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FOREIGN PATENT DOCUMENTS

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Signature*[Handwritten Signature]*

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Based on Form PTO/SB/08B (08-03) as modified by BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP on 09/10/03.